

		Subclass	ISSUE CLASSIFICATION
		Class	

FILED UNDER 35 U.S.C. 371

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. FF SCANNED <u>11/28</u> Q.A. <u>RG</u>	PATENT DATE
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APPLICATION NO. 09/530588	CONT/PRIOR D F	CLASS 257	SUBCLASS 15	ART UNIT 2811	EXAMINER A. J.
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KIMIHIRO MATSUSE
HAYASHI OTSUKI

WIRING STRUCTURE OF SEMICONDUCTOR DEVICE, ELECTRODE, AND METHOD FOR FORMING THEM

PTO-2040
12/99



ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner)	(Date)			
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____ _____ _____				ISSUE FEE	
	(Primary Examiner)	(Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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